

### **In The Specification**

Please amend the paragraph on page 1, lines 4-13, as follows:

“This application is related to several co-pending U.S. patent applications, namely serial no. 09/\_\_\_\_\_,\_\_\_09/728,634 entitled "CONVERTING ASYNCHRONOUS PACKETS INTO ISOCHRONOUS PACKETS FOR TRANSMISSION THROUGH A MULTI-DIMENSIONAL SWITCHED FABRIC NETWORK" filed concurrently herewith, serial no. 09/675,850 entitled "DISTRIBUTED RESOURCE RESERVATION SYSTEM FOR ESTABLISHING A PATH THROUGH A MULTI-DIMENSIONAL COMPUTER NETWORK TO SUPPORT ISOCHRONOUS DATA" filed on 9/28/00, and serial number 09/660,002 entitled "SWITCHED NODE COMPRISING A DISK CONTROLLER WITH INTEGRATED MULTI-PORT SWITCHING CIRCUITRY" filed on 9/11/00. The disclosures for the above referenced patent applications are incorporated herein by reference.”

### **In The Claims**

Please enter the following amendments to the claims:

- 1 1. (currently amended) An isochronous switched fabric network comprising:
  - 2 (a) a plurality of interconnected switched nodes forming multiple dimensions, each
  - 3 switched node comprising an upstream port and a downstream port for each
  - 4 dimension, each upstream and downstream port comprising an input port and an
  - 5 output port;
  - 6 (b) a discovery facility for discovering a depth of each dimension, and for discovering
  - 7 resources within each switched node;
  - 8 (c) an addressing facility, responsive to the discovery facility, for assigning a matrix
  - 9 address to each switched node;